

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant :	Rapport, et al.	Art Unit :	2841
Serial No. :	10/804,452	Examiner :	Yuriy Semchenko
Filed :	March 19, 2004	Conf. No. :	7279
Title :	MEMORY EXPANSION AND INTEGRATED CIRCUIT STACKING SYSTEM AND METHOD		

**MAIL STOP AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Applicants request consideration of the references listed on the attached PTO-1449 form. Under 37 C.F.R. § 1.98 (a)(2)(ii), only copies of foreign patent documents and/or non-patent literature are enclosed. Copies of any listed U.S. patents or U.S. patent application publications can be provided upon request.

This statement is being filed after a first Office action on the merits, but before receipt of a final Office action or a Notice of Allowance. All fees are being paid concurrently herewith on the Electronic Filing System (EFS) by way of Deposit Account authorization. Please apply all charges or credits to Deposit Account No. 06-1050, referencing Attorney Docket No. 21260-021001.

Respectfully submitted,

Date: 9/18/07



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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 21260-021001	Application No. 10/804,452
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary)		Applicant <b>Rapport, et al.</b>	
		Filing Date <b>March 19, 2004</b>	Group Art Unit <b>2841</b>
(37 CFR §1.98(b))			

### U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Document Date	Patentee		Subclass	File Nos.
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Examiner Signature	Date Considered
EXAMINER: Initiate citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

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		Filing Date March 19, 2004	Group Art Unit 2841

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Examiner Initial	Desig. ID	Document Number	Document Date	Patentee	Subclass	File Nos.
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Examiner Initial	Desig. ID	Document Number	Document Date	Patentee		Subclass	File Nos.
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(37 CFR §1.98(b))			

**Other Documents (include Author, Title, Date, and Place of Publication)**

Examiner Initial	Desig. ID	Document
		PCT/US2006/017015, International Search Report and Written Opinion dated October 17, 2006
		PCT/US2005/013345, International Preliminary Report on Patentability dated November 2, 2006
		PCT/US2005/016764; International Preliminary Report on Patentability dated November 23, 2006
		PCT/US2005/010756, International Search Report and Written Opinion dated October 12, 2006
		PCT/US2005/010756, International Preliminary Report on Patentability dated April 12, 2007
		PCT/US2005/039307, International Search Report and Written Opinion dated September 26, 2006
		PCT/US2005/013336, International Preliminary Report on Patentability dated November 9, 2006
		Howard W. Markstein, Western Editor, Rigid-Flex: A Maturing Technology dated February 1996, Electronic Packaging & Production
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		William R. Newberry, Xynetix Design Systems, Inc., Design Techniques for Ball Grid Arrays, 1997 published on the Internet

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